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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	180	703/14.ccls. and @pd>="20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/05/19 10:54
L4	1132	bidirectional and mixed and analog and digital and simulat\$5 and circuit	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/05/19 11:46



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bidirectional circuit simulation

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... -electrode semiconductor optical amplifiers and analysis of inline photodetection in **bidirectional** ...

A Sharaiha, M Guegan - Journal of Lightwave Technology, 2000 - OSA  
... and integration with electrical **circuit** components. ... Simultaneous **bidirectional** photodetection experiments using a three ... operation of a **bi-directional** bus made ...  
Cited by 15 - Related articles - Web Search - BL Direct - All 7 versions

Modeling, **simulation**, and experimental verification of soft-switched bi-directional DC-DC converters

H Li, FZ Peng, J Lawler - IEEE APEC proceeding, 2001 - ieeexplore.ieee.org  
... The proposed technique can be applied to other **bi-directional** DC-DC power ... average model are compared to those of detailed **circuit simulation** and experimental ...  
Cited by 15 - Related articles - Web Search

[PDF] ► **A simulation model for floating-gate MOS synapse transistors**

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems, 2002 - cs.washington.edu  
... 7]. To design large-scale synaptic **circuits**, engineers need an accurate SPICE **simulation** model for ... storage, locally computed **bidirectional** weight updates ...  
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Comparison of ESD protection capability of lateral BJT, SCR and **bidirectional** SCR for hi-voltage ...

V Vashchenko, A Concannon, M Ter Beek, P Hopper - Bipolar/BiCMOS Circuits and Technology Meeting, 2002. ..., 2002 - ieeexplore.ieee.org  
... using physical process and device **simulation** and pulse ... using a cylindrical lateral SCR and **bi-directional** SCR ... for hi-voltage (>24V) BiCMOS **circuits** presents a ...  
Cited by 10 - Related articles - Web Search - All 3 versions

Scan test data volume reduction in multi-clocked designs with safe capture technique

V Jain, J Waicukauski, S Inc, CA Mountain View - Test Conference, 2002. Proceedings. International, 2002 - ieeexplore.ieee.org  
... clock B. Figure 2: An example **circuit** showing clock ... of these clocks may lead to **simulation** Paper 6.2 ... corruption of old data due to **bi-directional** data transfer ...  
Cited by 16 - Related articles - Web Search - BL Direct - All 4 versions

Teaching nonlinear modeling, **simulation**, and control of electronic power converters using MATLAB/ ...

VF Pires, JFA Silva - IEEE transactions on education, 2002 - ieeexplore.ieee.org  
... suited to high power industrial applications and capable of **bidirectional** power flow ... sales/ads/analog/convergence.html [16] DW Hart, "Circuit simulation as an ...  
Cited by 32 - Related articles - Web Search - BL Direct - All 5 versions

Software-based self-testing methodology for processor cores- ► upc.es [PDF]

L Chen, S Dey - IEEE Transactions on Computer-Aided Design of Integrated ..., 2001 - ieeexplore.ieee.org  
... The modifications include: 1) splitting all **bidirectional** pins into ... in the normal operational mode of the **circuit**. ... Component-level fault **simulation** is used for ...

Cited by 113 - Related articles - Web Search - BL Direct - All 15 versions

[A new family of neural network-based \*\*bidirectional\*\* and dispersive behavioral models for nonlinear RF ...](#)

V Rizzoli, A Neri, D Masotti, A Lipparini - International Journal of RF and Microwave Computer-Aided ..., 2002 - interscience.wiley.com

... In addition, the **circuit** dynamics is often accounted for by ... In this article, we propose a **bidirectional** ANN- based ... DF are computed by the HB **simulation** [4] and ...

Cited by 9 - Related articles - Web Search - BL Direct - All 2 versions

[PDF] ► [Toward multiple-valued configurable random molecular logic units](#)

JM Seminario, LE Cordova - Proc. IEEE Nanotechnology Conf, 2001 - che.tamu.edu

... model [14] to include **bi-directional** operation of an ... diode effects in the **bidirectional** model are ... **Simulation** Program with Integrated **Circuit** Emphasis) program. ...

Cited by 19 - Related articles - Web Search - All 3 versions

[PDF] ► [A novel matrix converter topology with simple commutation](#)

L Wei, TA Lipo - CONFERENCE RECORD OF THE IEEE INDUSTRY APPLICATIONS ..., 2001 - ece.wisc.edu

... employed for a **circuit** level **simulation** to demonstrate ... previous proposed capacitorless DC link **circuits** [3],[4 ... except that the switches are all **bidirectional**. ...

Cited by 82 - Related articles - View as HTML - Web Search - BL Direct - All 2 versions

Key authors: [A Sharaiha](#) - [H Li](#) - [K Rahimi](#) - [V Jain](#) - [V Pires](#)



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... -electrode semiconductor optical amplifiers and analysis of inline photodetection in **bidirectional** ...

A Sharaiha, M Guegan - Journal of Lightwave Technology, 2000 - OSA  
... Simultaneous **bidirectional** photodetection experiments using a ... in presence of **bi-directional** transmission," in ... interconnect devices, **circuits**, and systems ...  
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[PDF] ► A **simulation** model for floating-gate MOS synapse transistors

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems, 2002 - cs.washington.edu  
... 7]. To design large-scale synaptic **circuits**, engineers need an accurate SPICE **simulation** model for ... storage, locally computed **bidirectional** weight updates ...  
Cited by 26 - Related articles - View as HTML - Web Search - BL Direct - All 7 versions

Hierarchical ATPG for **analog** circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - IEEE Design & Test of Computers, 2001 - ieeexplore.ieee.org  
... basis of component/device characteristics, **simulation**, or behavioral ... ds gives rise to a **bidirectional** edge ... can be modeled by additional **circuit** elements whose ...  
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Low voltage, low power, high performance current conveyors- ► [iitd.ac.in](http://iitd.ac.in) [PDF]

SS Rajput, SS Jamuar - Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE ..., 2001 - ieeexplore.ieee.org  
... voltage at which the proposed LVCM **circuit** will operate ... gdl P-Spice **simulation** of the LVCM structure ... voltage buffer (LVVB) and a **bi-directional** LVCM formed ...  
Cited by 9 - Related articles - Web Search - BL Direct - All 3 versions

A single-electron-transistor-based **analog/digital** converter

CH Hu, JF Jiang, QY Cai - Nanotechnology, 2002. IEEE-NANO 2002. Proceedings of the ..., 2002 - ieeexplore.ieee.org  
... a modified Tucker's inverter **circuit**, which has ... 1m-N.rci 2002 Fig. 6. **Simulation** results for ... nanowires and their application in **bi-directional** electron pumps ...  
Cited by 13 - Related articles - Web Search

Global exponential stability in continuous-time and discrete-time delayed **bidirectional** neural

...  
S Mohamad - Physica D: Nonlinear Phenomena, 2001 - Elsevier  
... We proceed in this section to consider the dynamical characteristics of a discrete-time **analogue** of the continuous-time **bidirectional** neural network (2.1). ...  
Cited by 108 - Related articles - Web Search - All 5 versions

A system-level **simulation** of complex multi-domain microsystems by using analogue hardware ...

M Jakovljevic, PA Fotiu, Z Mrcarica, H Detter - Sensors & Actuators: A. Physical, 2000 - Elsevier  
... in different specific domains, **bi-directional** coupling is ... applicability of system-level **simulation** is improved ... are generalized extensions of **circuit** simulators ...  
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[PDF] ► [Toward multiple-valued configurable random molecular logic units](#)  
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 ... model [14] to include **bi-directional** operation of an ... diode effects in the **bidirectional**  
 model are ... **Simulation Program with Integrated Circuit Emphasis**) program. ...  
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... [Verification for Electromigration Analysis in Arbitrary Shaped Metallization Patterns of Analog ...](#) - ► [tu-dresden.de](#) [PDF]  
 G Jerke, J Lienig - Proceedings of the conference on Design, automation and test ..., 2002 - portal.acm.org  
 ... an increased estimated lifetime for **bi-directional** and pulsed ... simply shaped layout  
 patterns in CMOS circuits. ... only one current- density **simulator** for **analog** ...  
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[CAD for RF circuits](#)- ► [psu.edu](#) [PDF]  
 P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ...,  
 2001 - portal.acm.org  
 ... other relevant to- pologies, including **bidirectional** or feedback ... numerical techniques  
 traditionally used in **analog** and **RF circuit simulation**, in particular ...  
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### 256-channel bidirectional optical interconnect using VCSELs and photodiodes on CMOS

DV Plant, MB Venditti, E Laprise, J Faucher, K ... - Journal of Lightwave Technology, 2001 - OSA

... to our knowledge, to send **bidirectional** data optically ... Photonics integrated with VLSI **circuits**," IEEE J ... characterization of a 2-D **bi-directional** free-space ...

Cited by 87 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 11 versions](#)

### A single-electron-transistor-based analog/digital converter

CH Hu, JF Jiang, QY Cai - Nanotechnology, 2002. IEEE-NANO 2002. Proceedings of the ..., 2002 - ieeexplore.ieee.org

... 1m-N.rci 2002 Fig. 6. **Simulation** results for ... and their application in **bi-directional** electron pumps ... Single-Electron Majority Logic **Circuits**", IEICE Trans. ...

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### Hierarchical ATPG for analog circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - IEEE Design & Test of Computers, 2001 - ieeexplore.ieee.org

... basis of component/device characteristics, **simulation**, or behavioral ... ds gives rise to a **bidirectional** edge ... can be modeled by additional **circuit** elements whose ...

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### A 90-dB SNR 2.5-MHz output-rate ADC using cascaded multibitdelta-sigma modulation at 8x

\*\*\*  
I Fujimori, L Longo, A Hairapetian, K Seiyama, S ... - IEEE Journal of Solid-State Circuits, 2000 - ieeexplore.ieee.org

... This is also included in the **simulation**. ... matching allowed a simple modification (**bi-directional** rotation) to ... the switches without any special **circuit** techniques ...

Cited by 116 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 5 versions](#)

### CAD for RF circuits- ▶ psu.edu [PDF]

P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ..., 2001 - portal.acm.org

... other relevant topologies, including **bidirectional** or feedback ... numerical techniques traditionally used in **analog** and RF **circuit simulation**, in particular ...

Cited by 11 - [Related articles](#) - [Web Search](#) - [All 16 versions](#)

### A behavioural opto-electro-thermal VCSEL model for simulation of optical links

F Mieveville, G Jacquemod, F Gaffiot, M Belleville - Sensors & Actuators: A. Physical, 2001 - Elsevier

... a vertical-cavity surface-emitting laser-based **bidirectional** free-space ... G. Jacquemod, Behavioral modeling for hierarchical **simulation** of optronic ... **Circuits Syst** ...

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### [PDF] ▶ A simulation model for floating-gate MOS synapse transistors

K Rahimi, C Diorio, C Hernandez, M Brockhausen - IEEE International Symposium on Circuits and Systems, 2002 - cs.washington.edu

... 7]. To design large-scale synaptic **circuits**, engineers need an accurate SPICE **simulation** model for ... storage, locally computed **bidirectional** weight updates ...

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... model [14] to include **bi-directional** operation of an ... diode effects in the **bidirectional** model are ... **Simulation** Program with Integrated **Circuit** Emphasis) program. ...  
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[A system-level \*\*simulation\*\* of complex multi-domain microsystems by using analogue hardware ...](#)

M Jakovljevic, PA Fotiu, Z Mrcarica, H Detter - Sensors & Actuators: A. Physical, 2000 - Elsevier  
... level modeling consisting of device, behavioral and **circuit** models in ... Heterogeneous **simulation** environments. ... in either a unidirectional or a **bi-directional** way. ...  
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[VLSI implementation of wireless bi-directional communication circuits for micro-stimulator-](#)

[► ncku.edu.tw \[PDF\]](#)

SC Li - 2002 - ethesys.lib.ncku.edu.tw  
... Figure 2-2. The internal **circuit** blocks of **bi-directional** implantable micro ... and through the **circuit simulator** to verify all **circuit** functions. If the **simulation** ...  
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Comparison of ESD protection capability of lateral BJT, SCR and **bidirectional** SCR for high voltage ...

V Vashchenko, A Concannon, M Ter Beek, P Hopper - Bipolar/BiCMOS Circuits and Technology Meeting, 2002. ..., 2002 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... of the BiT's and the **bi-directional** SCR's ... BiT (with 1k resistor in the base **circuit**) (a) and ... obtained by **mixed-mode** thermal coupled transient **simulation**. ...

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Verilog-AMS: **Mixed-signal simulation** and cross domain connectmodules- ► [columbia.edu](http://columbia.edu)

[PDF]

P Frey, DO'Riordan, CDS Inc, CA San Jose - ... International Workshop on Behavioral Modeling and **Simulation** ..., 2000 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

Page 1. Verilog- AMS: **Mixed-Signal Simulation** and Cross Domain Connect Modules ... extended Verilog-AMs **mixed-signal simulation** capabilities. 1 Introduction ...

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Low voltage, low power, high performance current conveyors- ► [iitd.ac.in](http://iitd.ac.in) [PDF]

SS Rajput, SS Jamuar - Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE ..., 2001 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... voltage at which the proposed LVCM **circuit** will operate ... gdl P-Spice **simulation** of the LVCM structure ... voltage buffer (LVVB) and a **bi-directional** LVCM formed ...

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Time-domain **simulation** of **mixed** nonlinear magnetic and electronicsystems- ► [soton.ac.uk](http://soton.ac.uk)

[PDF]

AD Brown, JN Ross, KG Nichols - IEEE Transactions on Magnetics, 2001 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... electromagnetic inter- actions are **bidirectional**) and analyzed ... extended to include magnetic **circuit** variables, allowing ... et al.: TIME-DOMAIN **SIMULATION OF MIXED** ...

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A behavioural opto-electro-thermal VCSEL model for **simulation** of optical links

F Mileyville, G Jacquemod, F Gaffiot, M Belleville - Sensors & Actuators: A. Physical, 2001 - Elsevier

... a vertical-cavity surface-emitting laser-based **bidirectional** free-space ... G. Jacquemod, Behavioral modeling for hierarchical **simulation** of optronic ... **Circuits Syst** ...

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P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ..., 2001 - [portal.acm.org](http://portal.acm.org)

... the **simulation** of a complex **mixed-signal** sys ... other relevant to- pologies, including **bidirectional** or feedback ... commonly used in RF **circuit simulation** are the ...

Cited by 11 - [Related articles](#) - [Web Search](#) - All 16 versions

Electrical and electrothermal 2D simulations of a 4H-SiC highvoltage current limiting device for

... F Nallet, A Senes, D Planson, ML Locatelli, JP ... - Power Electronics and Motion Control Conference, 2000. ..., 2000 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... study the ability of a **bi-directional** current limiting ... The current conduction is



**bidirectional** but the current ... The **circuit** used for the **mixed** mode **simulation** ...

[Cited by 5](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

**Bidirectional buffer with active pull-up/latch circuit for mixed-voltage applications**

HC Chow, CH Chien - US Patent 6,060,906, 2000 - Google Patents

... 11 illustrates voltage over time **simulation** results for the **circuit** of FIG. ... 5 shows a **bidirectional** multi-voltage buffer **circuit** 300 according to a first ...

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**A 1.1-V 270-/spl mu/A mixed-signal hearing aid chip**

DG Gata, W Sjurson, JR Hochschild, JW Fattaruso, L ... - IEEE Journal of Solid-State Circuits, 2002 - ieeexplore.ieee.org

... The **bidirectional** shift registers in Fig ... GATA et al.: 1.1-V 270- A **MIXED-SIGNAL HEARING AID** ... include analog and RF **circuit** design, **circuit simulation** and optimiza ...

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**A switch level fault simulation environment- ► uci.edu** [PDF]

V Krishnaswamy, J Casas, T Tetzlaff - Design Automation Conference, 2000. Proceedings 2000. 37th, 2000 - ieeexplore.ieee.org

... The fault **simulation** algorithm places no restrictions on the **circuit** styles used to implement designs. **Mixed** level simula- tion issues are discussed. ...

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Key authors: [L Chen](#) - [S Dey](#) - [P Frey](#) - [H Ahn](#) - [V Pires](#)



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Verilog-AMS: Mixed-signal simulation and cross domain connectmodules- ► [columbia.edu](#)

[PDF]

P Frey, DO'Riordan, CDS Inc, CA San Jose - ... International Workshop on Behavioral Modeling and Simulation ..., 2000 - [ieeexplore.ieee.org](#)

Verilog- AMS: Mixed-Signal Simulation and Cross Domain Connect Modules Peter Frey and Donald O'Riordan Cadence Design Systems, Inc.; 555 River Oaks Parkway ...

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Multithreaded, mixed hardware description languages logic simulation on engineering workstations

T Chan - US Patent 6,466,898, 2002 - Google Patents

... X Yes | No Apply stimulus to circuit signals Evaluate ... multi-phase clock # define these for mixed event-driven and cycle-based simulation # Cycle =( CLK ...

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Extending Spice-like analog simulator with a time-domain full-wavefield solver

T Li, W Sui, CDS Inc, N Providence - 2001 IEEE MTT-S International Microwave Symposium Digest, 2001 - [ieeexplore.ieee.org](#)

... dimensions, traditional analog or mixed-signal simulators ... and field (distributive) simulation through a simple and stable bi-directional circuit-field model. ...

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CAD for RF circuits- ► [psu.edu](#) [PDF]

P Wambacq, G Vandersteen, J Phillips, J ... - Proceedings of the conference on Design, automation and test ..., 2001 - [portal.acm.org](#)

... the simulation of a complex mixed-signal sys ... other relevant to- pologies, including bidirectional or feedback ... commonly used in RF circuit simulation are the ...

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Bi-directional mixed signal connection modules for automatic insertion

O Zinke, CD Syst, CA San Diego - Behavioral Modeling and Simulation, 2002. BMAS 2002. ..., 2002 - [ieeexplore.ieee.org](#)

BI-DIRECTIONAL MIXED SIGNAL CONNECTION MODULES FOR AUTOMATIC ... Abstract Efficient simulation of mixed signal ... describes how true bidirectional connect modules can ...

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VLSI implementation of wireless bi-directional communication circuits for micro-stimulator-

► [ncku.edu.tw](#) [PDF]

SC Li - 2002 - [ethesys.lib.ncku.edu.tw](#)

... Figure 2-2. The internal circuit blocks of bi-directional implantable micro ... and through the circuit simulator to verify all circuit functions. If the simulation ...

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Model engineering in mixed-signal circuit design: a guide to generating accurate behavioral models ...

SA Huss - 2001 - [books.google.com](#)

... 99 5.8 Generic VHDL-AMS model code for a 1 bit D/A converter 100 5.9 Simulation

of **mixed-signal circuits** 100 5.10 Step response of a dynamic system 103 ...

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### Teaching nonlinear modeling, **simulation**, and control of electronic power converters using MATLAB/ ...

VF Pires, JFA Silva - *IEEE transactions on education*, 2002 - [ieeexplore.ieee.org](#)

... power industrial applications and capable of **bidirectional** power flow ... (2001, September)

Simplorer **simulation** system ... 2001, September 6) SPECTRE **circuit simulator**. ...

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### Hierarchical ATPG for analog circuits and systems

M Soma, S Huynh, J Zhang, S Kim, G Devarayanadurg - *IEEE Design & Test of Computers*, 2001 - [ieeexplore.ieee.org](#)

... basis of component/device characteristics, **simulation**, or behavioral ... ds gives rise to a **bidirectional** edge ... can be modeled by additional **circuit** elements whose ...

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### ANALYSIS AND **SIMULATION** OF RF SUBSYSTEMS

J Roychowdhury - *Low-Power Design Techniques and CAD Tools for Analog and RF ...*, 2001 - [books.google.com](#)

... the  $tj-t2$  plane, or using a **mixed** time-frequency ... 2f0 Figure 2.16 HPSDs of

mixer-filter-mixer **circuit**. ... ANALYSIS AND **SIMULATION** OF RF SUBSYSTEMS 31 This transfer ...

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J Shen, JA Abraham - Journal of Electronic Testing, 2000 - Springer

... Current address: Jian Shen is now with **Cadence** Design Systems ... The AVPGEN test generation system from **Chandra** [13] uses constraint solving to choose initial data ...Cited by 37 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 4 versions](#)**On concurrent test of core-based SOC design**

Y Huang, WT Cheng, CC Tsai, N Mukherjee, O Samman, ... - Journal of Electronic Testing, 2002 - Springer

... cores, therefore, it can be tested concurrently with either Core 1, Core 2, or Core 3. In order to reduce the required number of **bi-directional** SOC pins ...Cited by 22 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 3 versions](#)**Astrophysics in 1999**

V Trimble, MJ Aschwanden - Publications of the Astronomical Society of the Pacific, 2000 - UChicago Press

... 1999), and the X-ray satellite **Chandra** (formerly AXAF ... Theoretical models of **bi-directional** EUV jets ejected from ... to detect (because a high **cadence** and contrast ...Cited by 1 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 3 versions](#)Key authors: **J Shen** - **J Abraham** - **Y Huang** - **W Cheng** - **C Tsai**[Google Home](#) - [About Google](#) - [About Google Scholar](#)



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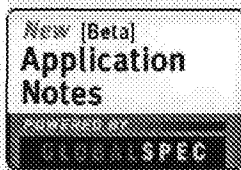
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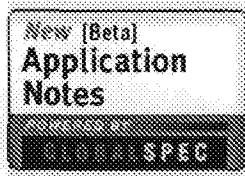
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